



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of)
)
 Shunpei Yamazaki et al.)
)
 Serial No.: 09/544,801)
)
 Filed: April 7, 2000)
)
 For: Semiconductor Device And Method)
 For Fabricating The Same)
)
 Art Unit: 2826)
)
 Examiner: Fazli Erdem)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to:
 the Assistant Commissioner for Patents ,
 P.O. Box 1450, Alexandria, VA 22313-1450 on
June 26, 2006
 (Date of Deposit)
Shannon Wallace
 Name of applicant, assignee, or Registered Rep.
Shannon Wallace 6/26/06
 Signature Date

Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

AMENDMENT G (AFTER FINAL)

Sir:

In response to the Final Rejection dated April 24, 2006, a RCE being filed herewith,
 please amend the above-identified application as follows: